

Application No. 09/993,917
Amendment in response to October 5, 2006 office action

IN THE SPECIFICATION

Please amend the paragraph at page 8, lines 8-12¹ in the original specification as follows:

FIG. 1 depicts two adjacent unit cells UC_1 and UC_2 located in the same column and in two adjacent rows. Each cell comprises a photodetector PD_i , a charge integration control unit 10, a unit cell charge integration capacitor CI_i and a charge ~~readout~~ transfer transistor TR_i . Transistors TR_i are controlled by line read signals $LnRd_i$ and the array includes a sense amplifier SA per every two unit cells UC, connected to the output of the transistors TR_i via a column line 12. As explained further below, charges from at least two integration capacitors CI_i of corresponding unit cells UC_i are summed at a common charge integration capacitor C associated with a sense amplifier SA.

¹ This appears as paragraph [0039] in the published application no. 2002/0186312.